



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Kazuhisa SAKAMOTO

Group Art Unit: 5681

Serial No.: 10/030,089 ✓

Examiner: P. Cao

Filed: January 14, 2002

Attorney Dkt. No.: 107400-00045

For: SEMICONDUCTOR DEVICE HAVING MOS FIELD-EFFECT TRANSISTOR

SUBMISSION OF FORMAL DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

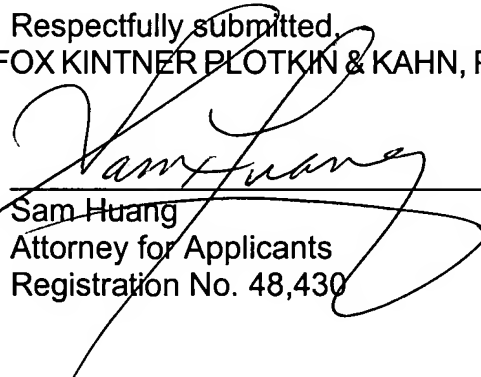
February 2, 2004

Dear Sir:

In response to the requirement to file formal drawings in the Notice of Allowability dated December 24, 2003, there are attached hereto three (3) sheets of formal drawings including Figs. 1, 2A, 2B, 3, and 4 to replace corresponding sheets containing the drawings. It is respectfully submitted that these formal drawings overcome the objections to the originally filed drawings.

If any fees are required with respect to this paper, please charge our Deposit Account No. 01-2300, **referencing Attorney Docket No. 107400-00045.**

Respectfully submitted,
ARENT FOX KINTNER PLOTKIN & KAHN, PLLC


Sam Huang
Attorney for Applicants
Registration No. 48,430

1050 Connecticut Avenue, N.W., Suite 400
Washington, D.C. 20036-5339
Tel: (202) 857-6000
Fax: (202) 638-4810

SH/grs